Workshop on Ultra Performance and Dependable Acceleration Systems (UPDAS)

held in conjunction with PDCAT'09

8th-11th Dec. 2009 @Hiroshima, Japan

http://www.arch.cs.titech.ac.jp/updas/

In this workshop, we focus on hardware architecture, system software, operating system and etc. for computing systems with accelerators. As next generation computing system, we address state-of-art issues for improvement of ultra performance and dependability. And also this workshop welcomes paper submissions about practical implementations, benchmarks, and dependability related accelerators.

TOPICS of particular interest include, but are not limited to:

- ✓Accelerators for high-performance computing
- ✓Program distribution to accelerators
- ✓Multi/Many-core architecture for accelerating computation
- ✓Dependable environment on accelerators
- ✓Operating systems for accelerators
- ✓Programming Support tools and programming models for accelerators
- ✓Benchmarks for performance and dependability of parallel programming
- ✓Embedded systems with accelerating architectures
- ✓Practical implementations with accelerating architectures
- ✓Network on Chip for accelerating architectures

IMPORTANT DATES

Deadline for submission	1st July, 2009
Acceptance notification	10th Aug., 2009
Camera-ready	1st Sep., 2009

SUBMISSION OF PAPERS

IEEE conference format, must not exceed 6 pages in length, and PDF format

Please submit your paper via updas@arch.cs.titech.ac.jp

All accepted papers will be included in conference proceedings of PDCAT'09, which will be published by IEEE Computer Society Press and automatically included in the IEEE Xplore digital library. At least one author of an accepted paper must register at the conference site and present the paper at the workshop.

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Hiroshima is good place for sightseeing, and to eat delicious food.